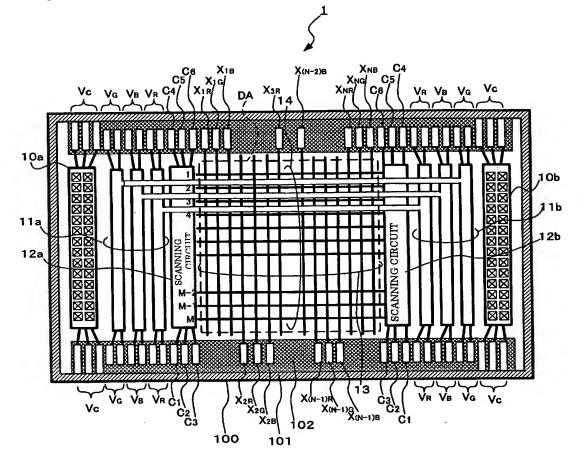
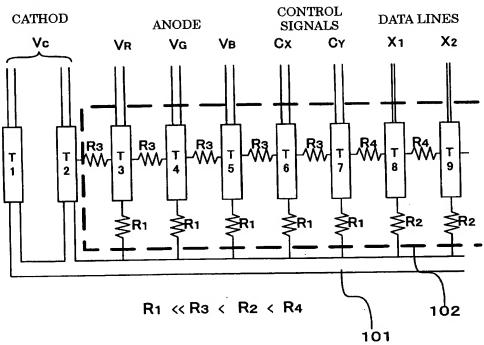


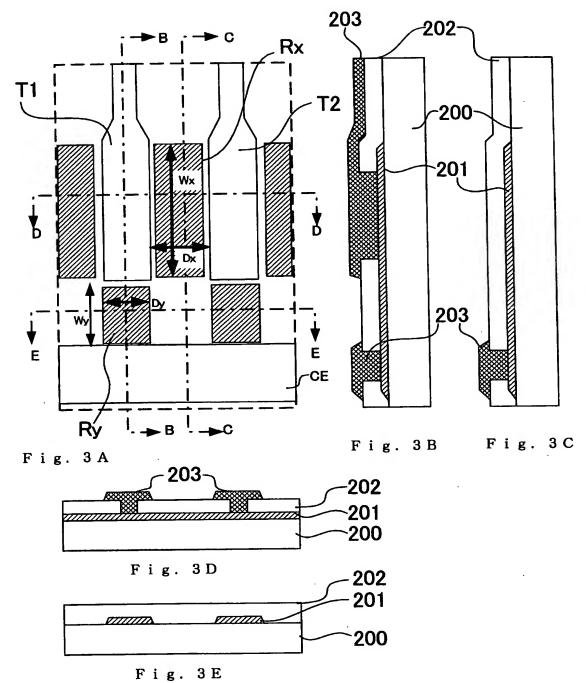
F i g. 1





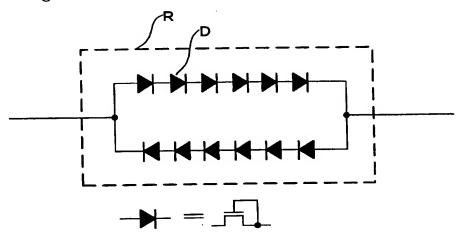


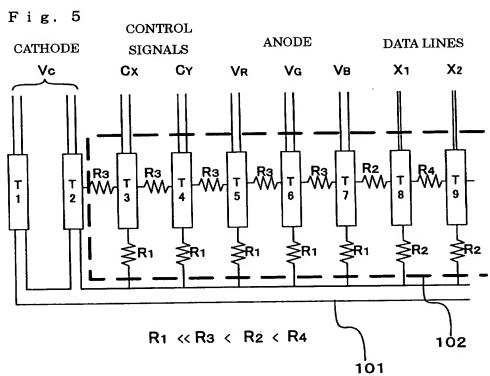






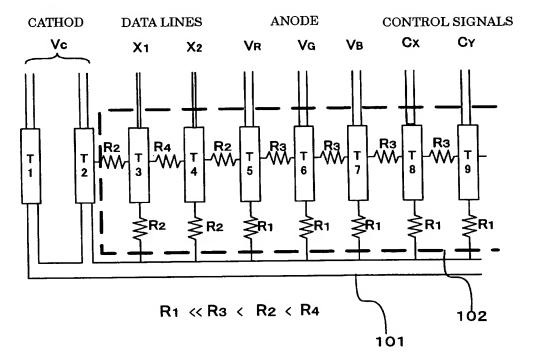
F i g. 4







F i g. 6

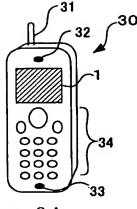




Vsig Vdd Vsig Vdd Vsel OLE Vsel SCANNING CIRCUIT Vsel 102

TERMINAL GROUP





F i g. 8A

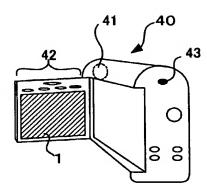
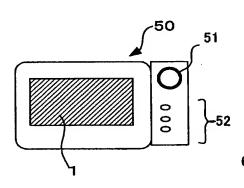


Fig. 8B



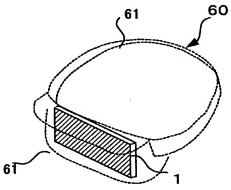


Fig. 8D

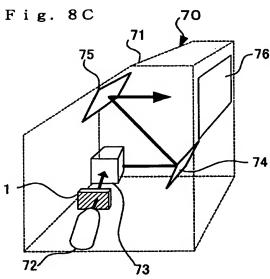


Fig. 8E

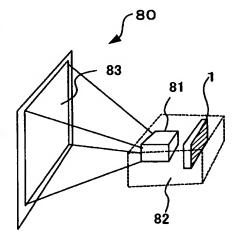


Fig. 8F